The CESM Test and Port Database

http://www2.cgd.ucar.edu/sections/cseg-cesm-software-engineering-group
https://csegweb.cgd.ucar.edu/testdb

CESM Breckenridge Workshop – June 17-20
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**In a nutshell...**

**What is it?** The CESM Test and Port Database is a web-based tool that tracks CESM release tag development, porting issues and test results.

**Why do we need it?** The growing complexity of supporting multiple release versions, a.k.a. tags, with evolving science capabilities and numerous computing platforms required a more sophisticated tracking system than the previous system of web pages.

**Who should use it?** All CESM developers, programmers and scientists interested in tracking CESM tag development, porting and testing. A CESM/CISM SVN developers login or NCAR/CGD UCAS login is all that is needed to access the database. Currently, only CSEG members have edit privileges.

**Need help?**
Please contact aliceb@ucar.edu.

https://csegweb.cgd.ucar.edu/testdb
Preview:

• Work-flow
• Relational Database Model
• Bottom-up / top-down
• Automated Test Results Reports with XML / CGI
• Incorporating User Feedback
• Future Directions
• Comments / Questions
Create New Planned Tag (Alpha Tag part of a Beta Group Tag)

Add Planned Component tags

Ready for prealpha testing?

Create Alpha / Beta / Release Tag

Add Alpha / Beta / Release Component tags

prebeta / prerelease testing complete?

Create final Release Tag in SVN
Previous Tag Tracking System

cesm1_1_0_rel04

Combined Tag Definition, Test Results, and Known Problems

Contents
- Tag Definition
- Test Definitions
- Software Engineering Test Record
- Raw Software Engineering Test Record
- Known Problems

Tag Definition

Tag name: cesm1_1_0_rel04
Originator(s): cseg
Date: Nov 15, 2012
Summary: Final tag for the cesm1 release

Answer Changes:
- E, E, F, I composite for rcp6/8.5 change answers after 2006

List all component tags that were used to create cesm tag:

<table>
<thead>
<tr>
<th>Tag</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>scripts</td>
<td>cesm1_1_0_rel_tags/cesm1_1_0_n25_scripts4_121105 ***</td>
</tr>
<tr>
<td>Machines</td>
<td>Machines_121114e ***</td>
</tr>
<tr>
<td>drv</td>
<td>cesm1_1_0_rel_tags/cesm1_1_0_n01_drvseq4_2_08 ***</td>
</tr>
<tr>
<td>mapping</td>
<td>mapping_121113b ***</td>
</tr>
<tr>
<td>share</td>
<td>share0_121022 ***</td>
</tr>
<tr>
<td>cam</td>
<td>cesm1_1_0_rel_tags/cesm1_1_0_n07_cam5_2_00 ***</td>
</tr>
<tr>
<td>clim</td>
<td>cesm1_1_0_rel_tags/cesm1_1_0_n08_clim4_0_54 ***</td>
</tr>
<tr>
<td>rcm</td>
<td>rcm1_0_09 ***</td>
</tr>
<tr>
<td>cico</td>
<td>cesm1_1_0_rel_tags/cesm1_1_0_n01_cico4_0_20120927 ***</td>
</tr>
<tr>
<td>cism</td>
<td>cism_121106a ***</td>
</tr>
</tbody>
</table>
Flat web pages with lots of scrolling....
Machines
- Machines_121114 (jedwards,bfb) *esm1_1_0_re104*
  - make user stacksize unlimited on lynx, yellowstone, hopper
- Machines_121114d (jedwards,bfb)
  - add BBQ machine mira
- Machines_121114c (jshollen,bfb)
  - update intel compiler, fix pugi LD_LIBRARY_PATH for frankfurt
- Machines_121114b (jedwards,bfb)
  - change machines support for web documents
- Machines_121114a (jedwards,bfb)
  - changes for intrepid
- Machines_121114 (jedwards,bfb)
  - changes for hopper
- Machines_121113 (jedwards,bfb)
  - update pe layout for janus
- Machines_1211106 (tcrails,bfb) *esm1_1_0_re103*
  - olympus fix
- Machines_121105a (jedwards,bfb) *esm1_1_0_re102*
  - change m60 testlayout on intrepid
- Machines_121105 (jedwards,bfb)
  - add ESMF_LIBDIR on yellowstone
- Machines_121101a (jedwards,bfb)
  - fix issue introduced in Machines_121101
- Machines_121101 (jedwards,bfb)
  - fix issue in configure, reduce duplicated perl code
- Machines_121100 (jedwards,bfb)
  - updates for yellowstone
- Machines_121029 (tcrails,bfb)
  - update undefined defaults, rename to env_mach_pat.xml, update 1 pe layouts, remove new layout (handled automatically by consaft)
- Machines_121025a (jshollen)
  - updated mpi run statement for frankfurt
- Machines_121025 (jedwards)
  - adjust pellayout for m60 on intrepid
- trunk_tags/Machines_121012a *esm1_1_0_re101*

Mapping
- trunk_tags/mapping_121113b (jlevy) *esm1_1_0_re104*
  - BugFix in gen_domain utility
- trunk_tags/mapping_121113 (jlevy)
  - BugFix in check_map utility
- trunk_tags/mapping_121105 (jlevy) *esm1_1_0_re102*
  - Add ocm2nd_map map (needed for trngid domain files)
- trunk_tags/mapping_121102 (jlevy)
  - New to working on yellowstone, need better ESMF install
- trunk_tags/mapping_121101 (jlevy)
  - Improved documentation / examples
- trunk_tags/mapping_121101a (jlevy)
... scroll down to test results – all pages edited manually

## Lynx Test Record

### Lynx prealpha Record

Possible test result outcomes:
- **UNDEF** - undefined result
- **FAIL** - scripts case generation failed
- **ERROR** - test setup error
- **GEN** - case generated
- **CTAIL** - check_case script failed, env variable or build error
- **BUILD** - model build passed, test not submitted
- **PEND** - case submitted
- **RUN** - case running or died during run, likely failure
- **PASS** - case passed
- **CHECK** - manual review of data is required
- **FAIL** - case failed
- **BFAIL** - compare test couldn’t find base result
- **ERROR** - test checker failed, test may or may not have passed

### Compiler: pgi

Test results last received: Thu Nov 15 17:04:09 2012

<table>
<thead>
<tr>
<th>Test</th>
<th>Status</th>
<th>Compare Baseline History</th>
<th>Memory Leak</th>
<th>Memory Compare</th>
<th>Throughput Compare</th>
<th>Baseline Tag</th>
<th>Case Status</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>SMS_Ld6.05_g16.BPASS</td>
<td>BFAIL</td>
<td>PASS</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>test completed 2012-11-14 15:07:20</td>
<td></td>
</tr>
</tbody>
</table>

### Compiler: intel

Test results last received: Thu Nov 15 17:04:54 2012

<table>
<thead>
<tr>
<th>Test</th>
<th>Status</th>
<th>Compare Baseline History</th>
<th>Memory Leak</th>
<th>Memory Compare</th>
<th>Throughput Compare</th>
<th>Baseline Tag</th>
<th>Case Status</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>ERS_PT_Ld3.f19.f19.FWSC:RUN</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>run started 2012-11-14 15:08:04 aborted during atm init, while opening ghg_forcing_2000_c110321.nc for reading</td>
</tr>
<tr>
<td>SMS_RLA.f45_f45.f</td>
<td>PASS</td>
<td>BFAIL</td>
<td>PASS</td>
<td></td>
<td></td>
<td></td>
<td>test completed 2012-11-15 11:53:52</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>test completed 2012-11-14</td>
<td></td>
</tr>
</tbody>
</table>
Testing and Porting Database Entity Relationship Diagram

Bottom up ....
CSEG | CESM SOFTWARE ENGINEERING GROUP

Community models must be state-of-the-science, easy to use, well documented, extensively tested, widely available, and efficient on a broad spectrum of modern computational platforms. Ensuring these attributes are maintained in all CGD's community models is the responsibility of CGD's CESM Software Engineering Group (CSEG) and the CESM Software Engineering Working Group (SEWG).

Related activities include responding to user help requests via a web bulletin board, managing all model releases, and continual monitoring and improving model performance.

CSEG's most important function is to provide modern Software Engineering support to all CGD sections and projects, as well as to all CESM Working Groups, including the Land Ice Working Group (LIWG), which otherwise has no direct NCAR involvement.

CSEG also runs the CESM test and port database for CESM Software developers to track and prioritize upcoming model changes, as well as to document associated problems.
User’s View

https://csegweb.cgd.ucar.edu/testdb
(SVN or UCAS authentication)

Only CSEG members are allowed to edit the database.
### CESM 1.3.0 Testing and Porting Database

#### Planned Tags

<table>
<thead>
<tr>
<th>Tag Name</th>
<th>Last Update</th>
<th>Planned Tag Summary</th>
</tr>
</thead>
<tbody>
<tr>
<td>cesm1_3_beta01 (white board)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>cesm1_3_alpha01d</td>
<td>Sean Santos</td>
<td>Fix for WACCM and strat chem bug in which negative constituent concentrations cause the model to crash in a CLY/BRY family adjustment routine.</td>
</tr>
<tr>
<td>cesm1_3_alpha01c</td>
<td>William Sacks</td>
<td>change intel and pgi to use -O0 when building in debug mode</td>
</tr>
<tr>
<td>cesm1_3_alpha01b</td>
<td>William Sacks</td>
<td>since this will likely change answers for all _D compsets, this alpha tag should be limited to this one Machines change</td>
</tr>
<tr>
<td>cesm1_3_alpha01a</td>
<td>James Edwards</td>
<td>splitting up the overcrowded cesm1_3_alpha01a, particularly in terms of CLM development</td>
</tr>
</tbody>
</table>

Showing 1 to 4 of 4 entries
Click on... Add New Planned Tag

CESM Testing and Porting Database – add a new planned tag

https://csegweb.cgd.ucar.edu/testdb/cgi-bin/addTag.cgi?tagType_id=1

CESM1_3_0 TESTING AND PORTING DATABASE

Add New Planned Tag Information

** Indicates a required field

** Beta Tag Group  -- Select --  or enter: 
(Note: A new alpha tag name will be automatically created based on the beta tag group selected or entered and following the CESM test tag naming conventions.)

** Planned Tag Summary

** Baseline Tag for comparison  -- Select --  or enter: 

Starting point tag name for SVN_EXTERNAL_DIRECTORIES
(Note: this tag name is used to read the SVN_EXTERNAL_DIRECTORIES URL most closely associated with this new tag as a starting point for all new external mods and additions. The field below will automatically be filled in with this tag name. You can also add default SVN externals in the Planned Tag view)

https://svn-ccsm-models.cgd.ucar.edu/

Answer Changes

Expected Test Failures
**Click on... cesm1_3_alpha01a**

**Individual Planned Tag View**

<table>
<thead>
<tr>
<th>Note Type</th>
<th>Last Update</th>
<th>Note</th>
</tr>
</thead>
</table>
| Answer Changes             | Sean Santos   | cism1_130514 changes answers for all CISM compsets
|                            | 2013-06-15    | scripts4_130522a changes answers for l_1850_CLM45_CN_GLIC_CISM1 (IG1850CLM45CN)                                                      |
|                            |               | Different forcing years for ICRU compsets.                                                                                         |
|                            |               | CAM-SE has a new ic file for ne30.                                                                                                  |
| Baseline Tag for Comparison|               | Double-click to edit                                                                                                              |
| Expected Test Failures     | Sean Santos   | CISM2 compsets will fail                                                                                                           |
|                            | 2013-06-15    | CAM-SE at ne30 will fail baseline tests.                                                                                             |
| Planned Tag Summary        | James Edwards | development beyond the 1.2 release                                                                                                  |
Scroll down... Individual Planned Tag View - Components

Options
- Migrate to Alpha Tag
- Full Change Log
- Brief Change Log
- View D8 SVN EXTERNALS
- Import SVN Externals

Tag Externals

<table>
<thead>
<tr>
<th>Who/When</th>
<th>Revision</th>
<th>Path</th>
<th>svnURL</th>
<th>bfb?</th>
<th>Exists?</th>
</tr>
</thead>
<tbody>
<tr>
<td>James Edwards</td>
<td>scripts4_130613</td>
<td>scripts</td>
<td><a href="https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130613">https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130613</a></td>
<td>all</td>
<td>Yes</td>
</tr>
<tr>
<td>Note</td>
<td></td>
<td></td>
<td>Fix create_clone to use -mach_dir</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Fix check_exactrestart to not fail on S compsset with no cpl logs to compare.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Remove rtm grid from ne240 compset and add ocn/ice domain mask</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Sean Santos</td>
<td>scripts4_130612</td>
<td>scripts</td>
<td><a href="https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130612">https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130612</a></td>
<td>all</td>
<td>Yes</td>
</tr>
<tr>
<td>Note</td>
<td></td>
<td></td>
<td>A minor fix to getTiming2.pl; explicitly use extended regex to override GREP_OPTIONS.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Alice Bartini</td>
<td>scripts4_130611</td>
<td>scripts</td>
<td><a href="https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130611">https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130611</a></td>
<td>all</td>
<td>No</td>
</tr>
<tr>
<td>Note</td>
<td></td>
<td></td>
<td>updating archive_metadata.sh to exclude more file types from being pushed up to the rundb svn repo including: poe.har*</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>csm.std*</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Jay Shollenberger</td>
<td>scripts4_130610</td>
<td>scripts</td>
<td><a href="https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130610">https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130610</a></td>
<td>all</td>
<td>Yes</td>
</tr>
<tr>
<td>Note</td>
<td></td>
<td></td>
<td>Re-Removed unneeded section from cism namelist documentation</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Jay Shollenberger</td>
<td>scripts4_130606</td>
<td>scripts</td>
<td><a href="https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130606">https://svn-ccsm-models.cgd.ucar.edu/scripts/trunk_tags/scripts4_130606</a></td>
<td>all</td>
<td>Yes</td>
</tr>
<tr>
<td>Note</td>
<td></td>
<td></td>
<td>add aux_wacsm testlist.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Click on... “+” to add a new component note
Click on... “Pencil icon” to edit an existing component note
Click on... Migrate to Alpha Tag

Process: Migrate Planned Tag to Alpha Tag and start testing
Display: Version: CESM1_2_0 and Alpha Tags tab

<table>
<thead>
<tr>
<th>Tag Name</th>
<th>Status</th>
<th>Last Update</th>
<th>Problems Identified After Tag Creation</th>
<th>Last Update</th>
<th>Answer Changes and Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>cesm1_2_beta08 (white board)</td>
<td></td>
<td></td>
<td>Ecosys cases will not work using given refcases. CISM2 compsets will not work in this tag.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>cesm1_2_alpha08b</td>
<td>Done</td>
<td>2013-05-07</td>
<td>Jay Stollenberger 2013-05-07</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>CISM2 compsets will not work in this tag.</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Eri Kluzaek 2013-03-14</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>None.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

- cism tag changes answers for GLC compsets
- datm changes answers but ONLY for I compsets with CRUNCEP forcing (CRU in the name)
- clm4_0_81 changes answers for ALL CLM45 compsets.
- clm4_0_80 changes answers for ALL CLM45 compsets.
- clm4_0_79 changes answers for CLM40
Click on... cesm1_2_alpha08b
cesm1_2_alpha08b == cesm1_2_beta08
Click on.. prealpha Tests
Test results sent to DB via XML test reporter and CGI listener
Click on... yellowstone Intel / mpich2 Prealpha Test Results

<table>
<thead>
<tr>
<th>Test</th>
<th>Overall Status</th>
<th>Compare Baseline History</th>
<th>Memory Leak</th>
<th>Memory Compare</th>
<th>Throughput Compare</th>
<th>Namelist Compare</th>
<th>Case Status</th>
<th>Automated Comment</th>
<th>Note</th>
</tr>
</thead>
<tbody>
<tr>
<td>CME.f45_g37_rx1.A.yellowstone_Intel.GC.a08bintel</td>
<td>PASS</td>
<td>PASS</td>
<td></td>
<td>PASS</td>
<td>PASS</td>
<td></td>
<td>test completed 2013-05-02 15:50:45</td>
<td></td>
<td>Double-cl to edit</td>
</tr>
<tr>
<td>ERL.T31_g37.IGCN.yellowstone_Intel.GC.a08bintel</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>test completed 2013-05-02 16:01:39</td>
<td></td>
<td>Double-cl to edit</td>
</tr>
<tr>
<td>ERS.f19_f19.FC4AQUAP.yellowstone_Intel.GC.a08bintel</td>
<td>PASS</td>
<td>BFAIL</td>
<td>PASS</td>
<td></td>
<td></td>
<td></td>
<td>test completed 2013-05-02 16:11:14</td>
<td></td>
<td>Double-cl to edit</td>
</tr>
<tr>
<td>ERS.T62_g37.G.yellowstone_Intel.GC.a08bintel</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>FAIL</td>
<td>PASS</td>
<td>test completed 2013-05-02</td>
<td></td>
<td>tput_decr = 0.4149999</td>
</tr>
</tbody>
</table>
Click on... Test Status == Fail to see aggregated test results for all testing platforms (likewise, click on Test Code name to see aggregated results for a given test across all platforms)

<table>
<thead>
<tr>
<th>Test Name</th>
<th>Machine</th>
<th>Compiler</th>
<th>mpilib</th>
<th>Report Date</th>
<th>Overall Status</th>
<th>Compare Baseline History</th>
<th>Memory Leak</th>
<th>Memory Compare</th>
<th>Throughput Compare</th>
</tr>
</thead>
<tbody>
<tr>
<td>SMS.f09_g16.TG.yellowstone_gnu.GC.a08bgnu</td>
<td>yellowstone</td>
<td>Gnu</td>
<td>mpich2</td>
<td>2013-05-03</td>
<td>FAIL</td>
<td>BFAIL</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ERI.T31_g37.IGCN.yellowstone_gnu.GC.a08bgnu</td>
<td>yellowstone</td>
<td>Gnu</td>
<td>mpich2</td>
<td>2013-05-03</td>
<td>FAIL</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ERS.T62_g37.G.yellowstone_gnu.GC.a08bgnu</td>
<td>yellowstone</td>
<td>Gnu</td>
<td>mpich2</td>
<td>2013-05-03</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
</tr>
<tr>
<td>SMS_Ly2.f09_g16_gl10.TGis2.yellowstone_intel.GC.a08bintel</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-03</td>
<td>FAIL</td>
<td>BFAIL</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SMS.T31_g37_gl10.BGCNis2.yellowstone_intel.GC.a08bintel</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-03</td>
<td>FAIL</td>
<td>BFAIL</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ERS.T62_g37.G.yellowstone_intel.GC.a08bintel</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-03</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
</tr>
<tr>
<td>ERS_PT.f19_g16.B20TR.janus_intel.GC.a08b</td>
<td>janus</td>
<td>Intel</td>
<td>openmpi</td>
<td>2013-05-03</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
<td>PASS</td>
</tr>
</tbody>
</table>
### CESM1_2_0 Testing and Porting Database

**Release Tags**

<table>
<thead>
<tr>
<th>Tag Name</th>
<th>Status</th>
<th>Created</th>
<th>Release Tag Summary</th>
<th>Not Included In Release</th>
</tr>
</thead>
<tbody>
<tr>
<td>cesm1_2_re106</td>
<td>Done</td>
<td>2013-06-03</td>
<td>Final tag for release and bug fixes</td>
<td>wrf, ww3</td>
</tr>
<tr>
<td>cesm1_2_re105</td>
<td>Done</td>
<td>2013-05-20</td>
<td>New initial conditions for CAM and CLM. Possible changes to CAM.</td>
<td>ww3, wrf</td>
</tr>
<tr>
<td>cesm1_2_re104</td>
<td>Done</td>
<td>2013-05-15</td>
<td>Fourth release tag. New initial conditions for 1850CRUCLM40CN. Forcing years for CRUNCEP updated.</td>
<td>ww3, wrf</td>
</tr>
<tr>
<td>cesm1_2_re103</td>
<td>Done</td>
<td>2013-05-13</td>
<td>Third release tag.</td>
<td></td>
</tr>
<tr>
<td>cesm1_2_re102</td>
<td>Done</td>
<td>2013-05-10</td>
<td>New initial condition files for CAM.</td>
<td></td>
</tr>
</tbody>
</table>

**Click on... Release Tags tab**
Click on... cesm1_2_rel03 and then click on namelists Tests
### Aggregated Namelists Comparison tests results

<table>
<thead>
<tr>
<th>Test Name</th>
<th>Machine</th>
<th>Compiler</th>
<th>mpiib</th>
<th>Namelist Compare</th>
<th>Case Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>SBN.1x1_urbanc_alpha.l1PT.yellowstone_intel.GC.rel03nlbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_urbanc_alpha.l1PT.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
<tr>
<td>SBN.1x1_tropicAtl.l20TR.yellowstone_intel.GC.rel03nlbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_tropicAtl.l20TR.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
<tr>
<td>SBN.1x1_tropicAtl.l1850CN.yellowstone_intel.GC.rel03nlbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_tropicAtl.l1850CN.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
<tr>
<td>SBN.1x1_tropicAtl.l1850CLM45.yellowstone_intel.GC.rel03nlbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_tropicAtl.l1850CLM45.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
<tr>
<td>SBN.1x1_smallvilleA.ICN.yellowstone_intel.GC.rel03nlbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_smallvilleA.ICN.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
<tr>
<td>SBN.1x1_smallvilleA.ICLM45CNCRP.yellowstone_intel.GC.rel03nlbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_smallvilleA.ICLM45CNCRP.yellowstone_intel.GC.rel03nlbeta</td>
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<td>PASS</td>
<td>SFAIL SBN.1x1_smallvilleA.ICLM45CN.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
<tr>
<td>SBN.1x1_numaIA.ICNCROP.yellowstone_intel.GC.rel03nlbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_numaIA.ICNCROP.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
<tr>
<td>SBN.1x1_numaIA.ICN.yellowstone_intel.GC.rel03nlbeta</td>
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<td>mpich2</td>
<td>PASS</td>
<td>SFAIL SBN.1x1_numaIA.ICN.yellowstone_intel.GC.rel03nlbeta</td>
</tr>
</tbody>
</table>
Click on... Comment Summary

Namelist Test Comment Summary - cesm1_2_rel03

Testnames that failed:

SBN.f09_g16.B1850BPRP.yellowstone_intel.GC.rel03nlbeta
SBN.f09_g16.B20TRBPRP.yellowstone_intel.GC.rel03nlbeta

Comment:

FAIL namelist compare: atm_in differs
  NEW: 
cd_data = '/glade/p/csem/cseg/inputdata/atm/cam/inic/fv/cami_1987-01-01_0.9x1.25_L26_c060703.nc'
  BASELINE: ncdata = 'b40.coup_carb.004.cam.1.0351-01-01-00000.nc'
FAIL namelist compare: ice_in differs
  NEW: 
cd_ice = '/glade/p/csem/cseg/inputdata/ice/cico.0001-01-01.gxlv6.20080212'
  BASELINE: ice_ic = 'b40.coup_carb.004.cice.r.0351-01-01-000000.nc'
FAIL namelist compare: ind_in differs
  NEW: 
finidat = '/glade/p/csem/cseg/inputdata/ccsm4_init/b40.1850.006.clm2_r.0863-01-01-000000.nc'
  BASELINE: finidat = 'b40.coup_carb.004.clm2.r.0351-01-01-000000.nc'
FAIL namelist compare: pop2_in differs
  NEW: 
init_ts_option = 'ccsm_startup'
  BASELINE: init_ts_option = 'ccsm_hybrid'
FAIL namelist compare: overflows_restart_type differs
  NEW: overflows_restart_type = 'ccsm_startup'
  BASELINE: overflows_restart_type = 'ccsm_hybrid'
FAIL namelist compare: init_ice_option differs
  NEW: init_ice_option = 'ccsm_hybrid'
  BASELINE: init_ice_option = 'ccsm_hybrid'
FAIL namelist compare: init_ecosys_init_file differs
  NEW: init_ecosys_init_file = '/glade/p/csem/cseg/inputdata/ccn/pop/gxlv6/ic/ecosys_jan_IC_gxlv6_corr02_Feb2013.nc'
  BASELINE: init_ecosys_init_file = 'same_as_78'
FAIL namelist compare: use_mnl_surf_vals differs
  NEW: use_mnl_surf_vals = .true.
  BASELINE: use_mnl_surf_vals = .false.

Testnames that failed:

SBN.f09_g16.B1850BDRD.yellowstone_intel.GC.rel03nlbeta
SBN.f09_g16.B20TRBDRD.yellowstone_intel.GC.rel03nlbeta

Comment:
Click on... Test Reports tab
## Filtered test results display

### Tag lists | Version: CESM1_2_0

#### Filtered Test Information

**Column Toggle Controls**
- Hide Test Name
- Hide Machine
- Hide Compiler
- Hide mpilib
- Hide Report Date
- Hide Testroot
- Hide Overall Status
- Hide Compare Baseline History
- Hide Memory Leak
- Hide Memory Compare
- Hide Throughput Compare
- Hide Nameist Compare
- Hide Baseline Tag
- Hide Case Status
- Hide Automated Comment
- Hide Note

### Show 10 entries

<table>
<thead>
<tr>
<th>Test Name</th>
<th>Machine</th>
<th>Compiler</th>
<th>mpilib</th>
<th>Report Date</th>
<th>Testroot</th>
<th>Overall Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>SBN.f09_g16.B1850BDRD.youngstone_intel.GC.a08bnl</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-07</td>
<td>/glade/scratch/jshollen/tests/a08bnl/</td>
<td>GEN</td>
</tr>
<tr>
<td>SBN.f09_g16.B1850BPRP.youngstone_intel.GC.a08bnl</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-07</td>
<td>/glade/scratch/jshollen/tests/a08bnl/</td>
<td>GEN</td>
</tr>
<tr>
<td>CME.T31_g37.B1850CN.youngstone_intel.GC.a08bintelbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-06</td>
<td>/glade/cmcm/jshollen/tests/a08bintelbeta/</td>
<td>PASS</td>
</tr>
<tr>
<td>ERI_PT.T31_g37.B1850CN.youngstone_intel.GC.a08bintelbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-06</td>
<td>/glade/cmcm/jshollen/tests/a08bintelbeta/</td>
<td>PASS</td>
</tr>
<tr>
<td>ERS.ne30_g16.B1850C5CN.youngstone_intel.GC.a08bintelbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-06</td>
<td>/glade/cmcm/jshollen/tests/a08bintelbeta/</td>
<td>PASS</td>
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<tr>
<td>ERS_D_E.T31_g37.B1850CN.youngstone_intel.GC.a08bintelbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-06</td>
<td>/glade/cmcm/jshollen/tests/a08bintelbeta/</td>
<td>PASS</td>
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<td>ERS_IOP.ne30_g16.B1850CN.youngstone_intel.GC.a08bintelbeta</td>
<td>yellowstone</td>
<td>Intel</td>
<td>mpich2</td>
<td>2013-05-06</td>
<td>/glade/cmcm/jshollen/tests/a08bintelbeta/</td>
<td>PASS</td>
</tr>
</tbody>
</table>
Future Directions...

• Continued incorporation of developer input
• More interaction with SVN for consistency
• Add structure to the whiteboards
Please contact Alice Bertini at aliceb@ucar.edu for any questions, problems, or suggestions.